

Future Product Brief

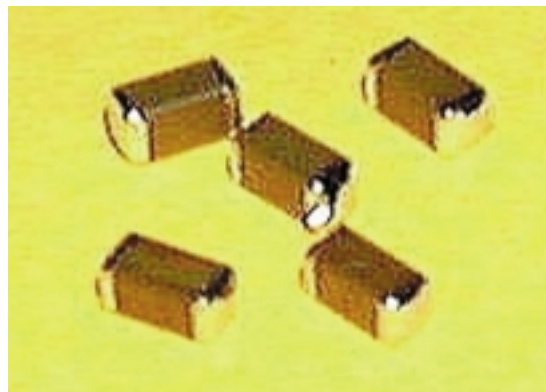


Low ESR – High Frequency Ceramic Chip Capacitors

NMC-L Series

FEATURES

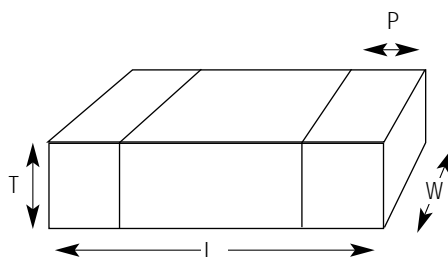
- Lower ESR For High Frequency (RF- Wireless) Applications
- NPO Dielectric For Stable Characteristics Over Time, Temperature And Voltage
- EIA Standard Case Sizes (0603 & 0805)
- Nickel Barrier Termination And Excellent Mechanical Strength



SPECIFICATIONS

Capacitance Range	0.5 ~ 47pF (1MHz, 1.0Vrms±0.2, +25°C)
Capacitance Tolerance	Below 10pF: ±0.1pF(B), ±.25pF(C), ±.5pF(D);
10pF and above	±1%(F), ±2%(G), ±5%(J)
Operating Temperature Range	-55°C ~ +125°C
Capacitance Change Over Temperature Range	0 ±30PPM/°C
Voltage Rating (WVDC)	50, 100VDC
Dissipation Factor	0.01 max. (1MHz, +25°C)
Insulation Resistance @ WVDC	100,000 MegΩ min. (+25°C)
Dielectric Withstanding Voltage	250% for 5 seconds max.

CASE SIZE	DIMENSIONS (mm)	CAPACITANCE VALUE	ESR @ 500MHZ
0603	Length (L): 1.6 ±0.15 Width (W): 0.8 ±0.15 Height (T): 1.0 MAX. Termination Width (P): 0.12 ~ 0.51	0.5 ~ 4.3pF	Pending test results
		4.7 ~ 20pF	0.385Ω Max.
		22 ~ 47pF	0.300Ω Max.
0805	Length (L): 2.0 ±0.20 Width (W): 1.25 ± 0.20 Height (T): 1.3 MAX Termination Width (P): 0.25 ~ 0.71	0.5 ~ 4.3pF	Pending test results
		4.7 ~ 20pF	0.385Ω Max.
		22 ~ 47pF	0.300Ω Max.



FUTURE ELECTRONICS



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Making the Difference